

STS Technology Steering Committee 2026

Committee Role	Name	Company/Organization	Division	Role
Chair	Junichi Mitani	Socionext	Global Leading Group, Production Management Team	Second Lead
Vice-Chair	Shin Nakagawa	SCREEN Semiconductor Solutions	R&D Strategy Operations Green Strategy	Manager
Vice-Chair	Takashi Nakamura	University of Osaka	Graduate School of Engineering	Visiting Professor
	Tsunetaka Akutagawa	ADVANTEST	SoC Marketing Department	Functional Manager
	Haruo Shimamoto	Advanced Industrial Science and Technology	Research Institute for Hybrid Functional Integration, 3D Integration Technology Research Group	Invited Researcher
	Kazunori Kato	AiT		President
Packaging Leader	Noboru Hayasaka	AiT		
	Noriaki Matsunaga	Applied Materials Japan	Japan Global Accounts	Sr Director, Technical
	Seiji Nagahara	ASML Japan	Japan Customer Team Technology	Technical Marketing Director
	Rocky Kobayashi	ATE Service		President & CEO
Lithography Leader	Kenichiro Mori	Canon	Semiconductor Production Equipment Unit	Senior Principal Engineer
	Shoji Uegaki	Crane Research		President
	Kazuyuki Sukou	Dai Nippon Printing	Fine Device Operations	Advisor
	Yasuhiro Shimura	EBARA		
	Shoji Hotta	Hitachi High-Tech	Nano-Technology Solution Business Group	Senior Strategist, Ph.D
	Daisuke Kenmochi	HOYA	New Product Development Project Team, Engineering Development Department, MASK SBU, HOYA GROUP LSI Division	
	Toyohiro Aoki	IBM Japan	Chiplet Interconnect, Semiconductor, IBM Research - Tokyo	Senior Research Scientist
	Yoshihiro Tomita	Intel	Advanced Package Integration, Technology Research	Principal Engineer

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	Suigen Kyoh	Kioxia	Forefront Process Research Department	Senior Manager
	Hajime Aoyama	Nikon	Development Sector, Semiconductor Business Unit, Precision Equipment Group	Senior Principal Member of Technical Staff
	Hidegori Akiyama	Nuvoton Technology Corporation Japan	Battery and Automotive Business Group, Product Engineering Department 1	Engineering Manager
M&I Leader	Hajime Nakamura	Onto Innovation Japan	Applications Engineering Department	Director, Applications Engineering
Testing Leader	Hiroshi Ito	Renesas Electronics	Test Platform Development Department HPC Business Management & Operations Division	Principal Engineer
	Masamichi Ido	Renesas Electronics	Test Platform Development Department HPC Business Management & Operations Division	Director
	Yasuhiro Uemoto	ROHM	Power GaN Solution & Product Development Div., LSI Development Headquarters	
Device/Process Leader	Masanobu Sato	SCREEN Semiconductor Solutions	R&D Strategy Operations	
	Yoshiya Hagimoto	Sony Semiconductor Solutions	Research & Development Division 2 Department 2	General Manager
	Yuichiro Yamazaki	TASMIT		CTO
	Toshio Konishi	Tekscend Photomask	Business Strategy	
	Sakura Nakamura	Teradyne		Sales Field Product Specialist
	Tsuyoshi Ishikawa	Tokyo Electron	Device Technology Planning Dept., Corporate Innovation Division	Expert
	Masafumi Asano	Tokyo Electron	Innovation X Lab.	Scientist, Director
	Shingo Morita	Tokyo Electron	ATS Business Planning Dept	
	Takashi Fujiwara	TOSHIBA Electronic Device & Storage	Semiconductor Div. Test Engineering Dept. Test Engineering Group I	Manager

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PowerDevice Leader	Yasuhiro Morikawa	ULVAC	PE-Semiconductor Technology Research Department Institute of Advanced Technology	Technology Manager (Global Technology Sensing Strategy)