

SEMICON Japan Standards Meetings 2025

TFT Building, Tokyo, Japan

- Technical Committee
- Task Force (TF) &Sub Group (SG)
- Standards Related Seminar & Events
- Non-Standards Program meeting

This schedule is subject to change without notice.

Japan Standard Time

8:00		9:00		10:00		11:00		12:00		13:00		14:00		15:00		16:00		17:00		18:00		19:00
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Wednesday, December 17, 2025

MEETING LOCATION

TFT Building
East 9th Floor, 3-6-11 Ariake, Koto-ku, Tokyo 135-0063 Japan

TFT Building East 9F	904		Parts & Materials ID Task Force 9:00-10:00	Traceability Japan Technical Committee 10:30-12:30		International Advanced Surface Inspection Task Force (AFM WG) 16:00-17:00	
	905				International Advanced Wafer Geometry Task Force 13:00-15:00	International Test Method Task Force 15:00-17:00	
	907		E84 over TCPIP Sub Group 9:00-10:30	Next Gen Assembly/Test Material Handling Task Force 10:30-12:00	PLP Panel FOUP TF 12:00 - 12:30		310mm Square Panel FOUP Task Force 16:00-16:45

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Thursday, December 18, 2025

MEETING LOCATION		<p style="color: blue;">TFT Building</p> <p style="color: blue;">East 9th Floor, 3-6-11 Ariake, Koto-ku, Tokyo 135-0063 Japan</p>	
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TFT Building East 9F	904		Silicon Wafer Japan Technical Committee 9:00-12:00		Japan RF Measurement Liaison Task Force 13:15-14:15	Metrics Japan Technical Committee 14:30-16:30	
	905		Physical Interfaces & Carriers Japan Technical Committee (PI&C) 9:00-12:00		GEM300 Task Force 12:30-14:30	Non-Standards Program Meeting 14:45-16:45	
	907					Compound Semiconductor Materials Japan Technical Committee 14:00-16:00	
	9-A						SEMI Standards Friendship Party & Awards Ceremony 17:00-19:00

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Friday, December 19, 2025

MEETING LOCATION		TFT Building East 9th Floor, 3-6-11 Ariake, Koto-ku, Tokyo 135-0063 Japan	
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TFT Building East 9F	904	<div style="border: 1px solid black; background-color: #e0f0e0; padding: 10px; width: fit-content; margin: auto;"> Information & Control (I&C) Japan Technical Committee 13:30-16:30 </div>	
	905		
	907		